

# 计算机研究与发展

## (Jisuanji Yanjiu Yu Fazhan)

第61卷 第6期 2024年6月

### 目 次

#### 处理器芯片跨层优化专题

遥感影像云检测和云去除方法综述 徐萌,王思涵,郭仁忠,贾秀萍,贾森(1585) 《计算机研究与发展》2022 年论文高被引 TOP10 (1608) 《计算机研究与发展》编委会 (封底	~~ (8)
遥感影像云检测和云去除方法综述徐萌,王思涵,郭仁忠,贾秀萍,贾森(158	5)
图形图像	
搜索服务响应时间异常诊断 夏思博,马明华,金鹏翔,崔丽月,张圣林,金娃,孙永谦,裴丹(157;	3)
MC-RHotStuff: 面向多链基于信誉的 HotStuff 共识机制 ············ 路宇轩, 孔兰菊, 张宝晨, 闵新平(1559	9)
一种基于 SHVE 的连接查询动态对称可搜索加密方案 ·························· 黄-才, 郁滨 (154)	5)
基于顶点划分和坐标标准化的密文域 3 维网格模型可逆信息隐藏 吕皖丽, 唐运, 殷赵霞, 罗斌 (1536	
mHealth 中细粒度策略隐藏和可追踪去中心访问控制方案 ························ 王静怡,阚海斌 (152!	5)
网络与信息安全	
联合数据增强的语义对比聚类 王气洪, 贾洪杰, 黄龙霞, 毛启容 (151:	1)
基于混合特征提取的流数据概念漂移处理方法 郭虎升, 刘艳杰, 王文剑(149	
持续学习的研究进展与趋势 ·················李文斌,熊亚锟,范祉辰,邓波,曹付元,高阳(1476	• /
人工智能	
	<i>(</i> )
SeChain: 基于国密算法的 RISC-V 安全启动机制设计与实现	(8)
·····································	0)
国密 SM4 算法 CBC 模式的高效设计与实现 ······	
面向处理器功能验证的硬件化 SystemVerilog 断言设计 ·············· 张子卿, 石侃, 徐烁翔, 王梁辉, 包云岗 (1436	6)
高性能自研处理器物理设计频率提升方法何小威,乐大珩,郭维,隋兵才,邓全(1429	
面向多核 CPU 与 GPU 平台的图处理系统关键技术综述 ····································	1)
·····································	8)
Direct xPU: 一种新型节点间通信优化的分布式异构计算架构 ·····	
容错深度学习加速器跨层优化 张青,刘成,刘波,黄海同,王颖,李华伟,李晓维(1376	0)
机器学习辅助微架构功耗建模和设计空间探索综述 翟建旺,凌梓超,白晨,赵康、余备(135)	1)
前 言李华伟,哈亚军,姚海龙 (134)	9)

责任编辑: 黄冠华 互校编辑: 沈宝丽

### JOURNAL OF COMPUTER RESEARCH AND DEVELOPMENT

Vol. 61 No. 6 June 2024

#### **CONTENTS**

#### Special Issue on Cross-layer Optimization of Processor Chips

Preface	349)
Machine Learning for Microarchitecture Power Modeling and Design Space Exploration: A Survey	
Zhai Jianwang, et al. (13	351)
Cross-Layer Optimization for Fault-Tolerant Deep Learning Accelerators · · · · Zhang Qing, et al. (13)	370)
Direct xPU: A Novel Distributed Heterogeneous Computing Architecture Optimized for Inter-node	
Communication Optimization	388)
Survey on Key Technologies of Graph Processing Systems Based on Multi-core CPU and GPU Platforms	
Zhang Yuan, et al. (14	401)
Promoting Frequency Method for Our Own High Performance Processor Physical Design ···· He Xiaowei, et al. (14	429)
Design of SystemVerilog Assertions Hardware Towards Efficient Processor Functional Verification	
Zhang Ziqing, et al. (14	436)
Efficient Design and Implementation of SM4 Algorithm with CBC Mode · · · · · · · · · · · · · · · · · · ·	450)
SeChain: Design and Implementation of RISC-V Secure Boot Mechanism Based on Domestic Cryptographic	
Algorithms ····· Rui Zhiqing, et al. (14	458)
Artificial Intelligence	
Advances and Trends of Continual Learning	476)
Concept Drift Processing Method of Streaming Data Based on Mixed Feature Extraction ··· Guo Husheng, et al. (14	497)
Semantic Contrastive Clustering with Federated Data Augmentation	511)
Network and Information Security	
Fine-Grained Policy-Hiding and Traceable Decentralized Access Control Scheme in mHealth	
	525)
Reversible Data Hiding for 3D Mesh Model in Encrypted Domain Based on Vertex Partition and Coordinate	
Standardization	536)
Dynamic Searchable Symmetric Encryption Scheme for Conjunctive Queries Based on SHVE······	
Huang Yicai, et al. (15	545)
MC-RHotStuff: Multi-Chain Oriented HotStuff Consensus Mechanism Based on Reputation	\
Lu Yuxuan, et al. (15	
Response Time Anomaly Diagnosis for Search Service · · · · · · · · · · · · · · · · · · ·	573)
Graphic Images	
Review of Cloud Detection and Cloud Removal Methods for Remote Sensing Images	585)
Editorial Columns	ver)